Re. Point V.   **10/562303**

1   This decision makes reference to the following documents:

D1 : GUERRA L ET AL:"Cycle and Phase accurate 56 DSP
     modeling and integration for HW/SW co-verification"
     DESIGN AUTOMATION CONFERENCE, 1999. PROCEEDINGS. 36TH
     NEW ORLEANS, LA, USA 21-25 June 1999, PISCATAWAY, NJ,
     USA,IEEE, US, 21 June 1999 (1999 06-21), pages 964-
     969, XP010344032 ISBN:1-58113-092-9

2   Independent claims 1, 6 and 11

2.1 The present application does not fulfill the requirements
    of Article 33(1) PCT because the object of Claim 1 is not
    novel in the sense of Article 33(2) PCT.

    Document D1 discloses (the references in brackets relate to
    this document) a system for combining and representing
    signals of a hardware simulation device (Figure 1 "Bus Pin
    Model") and elements of a program listing (page 965, left
    column, lines 36 - 38),
        with the hardware simulation device simulating the
    behavior of a circuit with a processor, a program memory
    which contains the program code of the program (page 968,
    left column, line 1), and application-specific hardware
    components and creating signals as the result of the
    simulation creates (page 964, left column lines 29 - 31),
        with the elements of the program listing being combined
    with the signals created during the simulated execution of
    the program code contained in the program memory
    corresponding to these elements (page 967, left column,
    lines 15 - 25),
        with the elements of the program listing being able to
    be represented in a first partial area of a graphical
    display means and the signals in a second partial area of

the display means (Figure 7). The object of claim 1 is thus not novel.

2.2 These objections also apply to the corresponding claims 6 and 11. Therefore the object of claims 6 and 11 is not novel.

3   DEPENDENT CLAIMS 2-5, 7-10

Claims 2-5, 7-5 do not contain any features, which in combination with the features of any claim to which they relate, fulfill the requirements of the PCT in relation to novelty or inventive step;

4   INDUSTRIAL APPLICABILITY

The object of claims 1-11 is industrially applicable for circuit development.